

Texas Instruments Technical Seminar  
Sponsored by TI & Seltec Sales  
EEE&M 2010 Show  
St. Charles Convention Center  
Wednesday, April 7<sup>th</sup>, 11:15AM – 12:45PM  
Seminar Room 103

## Circuit Isolation Techniques and Implementation

Multiple options are now available to electronics designers to implement galvanic isolation. Apart from isolation technologies like capacitive, optical and inductive/magnetic to choose from, they must also contend with the various isolation standards regarding voltage ratings and creepage/clearance distances. This discussion intends to simplify the decision making associated with choosing the right isolation solution.

To register for this seminar please contact Mike Long at Seltec Sales, 636-634-2210 or [mike.long@seltec-sales.com](mailto:mike.long@seltec-sales.com)